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GGE Gielen, HCC Walscharts, WMC Sansen - Solid-State Circuits, IEEE Journal of, 1989 - ieeexplore.ieee.org
... Secondly, the explicit representation of **mismatch** terms allows the ... The output **impedance** is now given by Rj(g2ç1 ... 4. The sym- (5) bolic **simulator** clearly gives ...
Cited by 101 - Web Search

Characterization and modeling of in-building power lines for high-speed data transmission

LT Tang, PL So, E Gunawan, YL Guan, S Chen, TT Lie - Power Delivery, IEEE Transactions on, 2003 - ieeexplore.ieee.org
... The **impedance mismatch** will, therefore, invalidate the re- sults ... caused by reflections at **impedance** disconti- nuities ... PLC channel for software **simulation** or to ...
Cited by 21 - Web Search - BL Direct

Performance and **Simulation** of the RICE detector - group of 6 »

I Kravchenko... - Arxiv preprint astro-ph/0112372, 2001 - arxiv.org
... struck" dipole receivers in a **simulation** event, along ... this **frequency** is evident in this Fourier Transform. ... measurements of the complex antenna **impedance** Z ...
Cited by 44 - View as HTML - Web Search

Switched currents-a new technique for analog sampled-data signal processing

JB Hughes, NC Bird, IC Macbeth - Circuits and Systems, 1989., IEEE International Symposium on, 1989 - ieeexplore.ieee.org
... system building as demonstrated by the **simulation** of a ... to that of threshold voltage **mismatch** except that ... of accuracy due to the urge **impedance** mismaich which ...
Cited by 97 - Web Search

Frequency-domain interferometer **simulation** with higher-order spatial modes - group of 11 »

A Freise, G Heinzel, H Luck, R Schilling, B Willke ... - Class. Quantum Grav, 2004 - electronicjournals.iop.org
... of γ and the mode **mismatch** parameter K ... the corresponding dark-fringe pattern computed by the **simulation**. ... At some point the PRC becomes **impedance**- matched where ...
Cited by 13 - Web Search - BL Direct

Identification of **transfer** functions with time delay and its application to cable fault location - group of 2 »

R Pintelon, L Van Biesen - Instrumentation and Measurement, IEEE Transactions on, 1990 - ieeexplore.ieee.org
... a rational approximation of the generator **mismatch**, the fault ... also true for the same **simulation** done with ... reflectometers match the generator **impedance** ZK with ...
Cited by 12 - Web Search

DRAFTS: discretized analog circuit fault simulator - group of 2 »

N Nagi, A Chatterjee, JA Abraham - Proceedings of the 30th international conference on Design ..., 1993 - portal.acm.org
... variations in the **impedance** of the short. ... are also caused by process gradients which produce device **mismatch**. ... Section 2, the fault **simulator** takes the network ...
Cited by 29 - Web Search - BL Direct

Crosstalk analysis of interconnection lines and packages in high-speed integrated circuits -

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Realisation of current-mode **transfer function** using four-terminalfloating nullor - group of 2 »
M Higashimura - Electronics Letters, 1991 - ieeexplore.ieee.org
... the circuits to be cascaded without requiring any **impedance matching** device. Conclusion: Active-RC synthesis of a current-mode **transfer function** using an FTFN ...
Cited by 32 - Web Search

Automatic **impedance matching** with a neural network - group of 2 »
M Vai, S Prasad - Microwave and Guided Wave Letters, IEEE [see also IEEE ..., 1993 - ieeexplore.ieee.org
... AUTOMATIC **IMPEDANCE MATCHING** Recently, researchers have applied the neural computing ... are qualitatively analyzed in Table to its **transfer function** and delivers ...
Cited by 22 - Web Search - BL Direct

VLSI architecture for 2D Daubechies wavelet transform withoutmultipliers
AS Lewis, G Knowles - Electronics Letters, 1991 - ieeexplore.ieee.org
... the circuits to be cascaded without requiring any **impedance matching** device. Conclusion: Active-RC synthesis of a current-mode **transfer function** using an FTFN ...
Cited by 82 - Web Search

The acoustics of the vocal tract in the horseshoe bat, Rhinolophus hildebrandti - group of 2 »
DJ Hartley, RA Suthers - The Journal of the Acoustical Society of America, 1988 - link.aip.org
... Changes in the **transfer function** caused by filling the nasal chambers suggest that these structures may play an **impedance matching** role at the second harmonic. ...
Cited by 8 - Web Search

Bandpass sigma-delta modulation - group of 2 »
R Schreier, M Snelgrove - Electronics Letters, 1989 - ieeexplore.ieee.org
... However, alternative composite structures for **impedance-matching** could he developed to ... of a modulator with the selection of H, the error **transfer function**. ...
Cited by 73 - Web Search

An analytic and experimental comparison of direct and external modulation in analog fiber-optic ... - group of 3 »
CH Cox III, GE Betts, LM Johnson - Microwave Theory and Techniques, IEEE Transactions on, 1990 - ieeexplore.ieee.org
... V , , the incremental **transfer function** for a MZ external modulator is (7) Thus, to maximize the modulated optical power, the **impedance matching** circuit should ...
Cited by 56 - Web Search - BL Direct

Realization of current mode all-pass networks using a current conveyor - group of 2 »
M Higashimura, Y Fukui - Circuits and Systems, IEEE Transactions on, 1990 - ieeexplore.ieee.org
... 19], there is no network which realizes an all-pass **transfer function** in current ... the networks may be used in cascade without requiring **impedance matching** device ...
Cited by 29 - Web Search

An estimation method of the **transfer function** of indoor power-line channels for Japanese

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Sensitivity of human subjects to head-related **transfer-function** phase spectra - group of 3 »

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A Kulkarni, SK Isabelle, HS Colburn - The Journal of the Acoustical Society of America, 1999 - link.aip.org
... "**Simulation** of free-field sound sources and its ... of the head-related-**transfer-function**," "Binaural detection ... model of the **impedance matching** properties of ...
Cited by 24 - Web Search - BL Direct

Automatic **impedance matching** with a neural network - group of 2 »
M Vai, S Prasad - Microwave and Guided Wave Letters, IEEE [see also IEEE ..., 1993 - ieeexplore.ieee.org
... Often, the **transfer function** sums the incoming signals ... **IMPEDANCE MATCHING** NETWORK SYNTHESIS One obvious use of ... stub- networks by means of computer **simulation**. ...
Cited by 22 - Web Search - BL Direct

Bandpass sigma-delta modulation - group of 2 »
R Schreier, M Snelgrove - Electronics Letters, 1989 - ieeexplore.ieee.org
... alternative composite structures for **impedance-matching** could he ... is presented, with **simulation** results lor ... the selection of H, the error **transfer function**. ...
Cited by 73 - Web Search

Efficient **simulation** of NPR for the optimum design of satellitetransponders SSPAs
J Lajoinie, E Ngoya, D Baralaud, JM Nebus, J ... - Microwave Symposium Digest, 1998 IEEE MTT-S International, 1998 - ieeexplore.ieee.org
... account for pos- sible amplifier **impedance mismatch** and biasing ... As regards NPR **simulation** the noise generated ... band-pass applications the **transfer function** H, (f ...
Cited by 7 - Web Search - BL Direct

Electrical **impedance mismatch** in capacitive micromachined ultrasonic transducers - group of 2 »
A Caronti, G Caliano, A Iula, M Pappalardo - Ultrasonics Symposium, 2000 IEEE, 2000 - ieeexplore.ieee.org
... **SIMULATION** RESULTS ... attenuation being produced by the input and output **impedance mismatch**. ... improvement in sensitivity due: to !be **impedance matching** comes at ...
Cited by 3 - Web Search - BL Direct

Statistical analysis and **simulation** of indoor single-phase low voltage power-line communication ...
... , CN Capsalis, CG Karagiannopoulos, NJ Theodorou - Consumer Electronics, IEEE Transactions on, 2003 - ieeexplore.ieee.org
... Being the line matched, its **transfer function** is given as ... et al.: Statistical Analysis and **Simulation** of Indoor Single ... Fig. 2. Points of **impedance mismatch**, ...
Cited by 5 - Web Search - BL Direct

Measurement and **simulation** of the voltage-flux **transfer function** of SQUID arrays - group of 3 »
K Li, SP Hubbell - Applied Superconductivity, IEEE Transactions on, 1995 - ieeexplore.ieee.org
... 1995 IEEE Measurement and **Simulation** of the Voltage-Flux **Transfer Function** of SQUID arrays ... the dynamic range, bandwidth, **impedance matching**, and simplili ...

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Architecture, design, and application of an **event-based** test system **All articles** Recent articles
R Rajsuman - Instrumentation and Measurement, IEEE Transactions on, 2003 - ieeexplore.ieee.org
... that is identical to the design **simulation**. ... test, and connectivity tests including flying **probes**. ... Weekly **code coverage** analysis (both functional and line ...
Web Search - BL Direct

[BOOK] System-On-A-Chip Verification: Methodology and Techniques
P Rashinkar, P Paterson, L Singh - 2001 - books.google.com
... Signal **Coverage** 121 3.9.2 Performing **Code Coverage** Analysis 121 ... DAC 143 4.6.2 Creating the **Netlist** 146 4.6.3 ... 5 **Simulation** 153 5.1 Functional **Simulation** 154 5.2 ...
Cited by 39 - Web Search

Limitations and challenges of computer-aided design technology for CMOS VLSI - group of 11 »
R Bryant, KT Cheng, A Kahng, K Keutzer, W Maly, R ... - Proceedings of the IEEE, 2001 - ieeexplore.ieee.org
... to a logic-gate-level representation (logic **netlist**) using the ... a single mask set and **probe** card costs $1 ... **Code coverage** fails to measure the occurrence of event ...
Cited by 20 - Web Search - BL Direct

ACHIEVING SCALABLE HARDWARE VERIFICATION WITH SYMBOLIC **SIMULATION** - group of 2 »
V Bertacco - 2003 - eecs.umich.edu
... the ability to mix cycle-based and **event-based simulation**. ... design under test are visited during each **simulation** ... is commonly used in industry is state **coverage**. ...
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Development and Verification of fast C/C++ Models for the Star12 Micro Controller - group of 2 »
J Schlosser - schlosser.info
... between the functions is no longer **event-based**, but a ... this level a first performance **simulation** is possible. ... **Code** optimizations take place as well as adoptions ...
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verificationavenue - group of 11 »
B Regards - synopsis.com
... designs and provides links to **code** for easy ... of Formality ® 2003 expands datapath **coverage**, improves datapath ... performance when the gate level **netlist** has been ...
View as HTML - Web Search

A Device-Level FPGA Simulator
JE Hunter III - 2004 - scholar.lib.vt.edu
... 5.4 HelloTile example **code** . . ... is used to create a **netlist** of all internal connections. ... Unlike JRoute in JBits2.8, ADB provides complete device **coverage** and is ...
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Functional **Verification** of Large ASICs - group of 11 » **All articles** Recent articles
A Evans, A Silburt, G Vrckovnik, T Brown, M ... - Design Automation Conference, 1998 - doi.ieeecomputersociety.org
... 5.3 **Coverage** Overlap In comparing the three ... for productivity improvement through faster **simulation** or coding ... drastically reduce the overall **verification** effort ...
Cited by 28 - Web Search - BL Direct

Next generation COTS-commercial IP blocks in avionics
H Forsberg, P Bjureus, I Soderquist, P Lamaty, D ... - Digital Avionics Systems Conference, 2004. DASC 04 The 23rd, 2004 - ieeexplore.ieee.org
... that the IP has been verified with advanced **verification** methods. ... languages, pre-compiled or not), Tesivectors (**simulation scripts**, generators), Synthesis ...
Cited by 1 - Web Search

Functional **Verification** Methodology of Chameleon Processor - group of 8 »
F Casaubieilh, A Mclsaac, M Benjamin, M Bartley, F ... - 33rd Design Automation Conference, Las Vegas, June, 1996 - doi.ieeecomputersociety.org
... emulation is introduced into the **verification** process only ... 3.6 Code **Coverage** A code **coverage** analysis is ... at least once during the regression test **simulation**. ...
Cited by 25 - Web Search - BL Direct

A methodology for design **verification**
E Hu, B Yeh, T Chan, AC Inc, CA Santa Clara - ASIC Conference and Exhibit, 1994. Proceedings., Seventh ..., 1994 - ieeexplore.ieee.org
... tapeout of the device to assure maximal **coverage** in a ... run in a random fashion during a **simulation** run. ... engineer can be aided by a **verification** engineer skilled ...
Cited by 1 - Web Search

Simulation-Based Approximate Global Fault Collapsing
H Al-Assad, R Lee - Proc. International Conf. on VLSI, 2002 - ece.ucdavis.edu
... with the circuit description in a **netlist** format and ... a considerably large speed up in **simulation** time and ... faults produced test sets with high **coverage** of all ...
Cited by 7 - View as HTML - Web Search

An Effective Framework for Enabling the Reuse of External Soft IP
S Sarkar, G Subash Chandar - doi.ieeecomputersociety.org
... metrics (like gate count, **verification coverage**, test **coverage** etc ... Design **Verification** (DV ... Test-bench Creation, Stimulus Generation, **Simulation**, Regression Setup ...
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Remote Path Delay Fault **Simulation** - group of 2 »
Ø Gjermundnes, EJ Aas - Digital System Design, 2005. Proceedings. 8th Euromicro ... - doi.ieeecomputersociety.org

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   Ismail, Y.I.; Friedman, E.G.;
   Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction
   Volume 21, Issue 2, Feb. 2002 Page(s):131 - 144
   Digital Object Identifier 10.1109/43.980254
   AbstractPlus | References | Full Text: PDF(278 KB) IEEE JNL
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2. **Computational models of transmission lines with skin effects and dielect**
   Qingjian Yu; Wing, O.;
   Circuits and Systems I: Fundamental Theory and Applications, IEEE Transacti
   Circuits and Systems I: Regular Papers, IEEE Transactions on]
   Volume 41, Issue 2, Feb. 1994 Page(s):107 - 119
   Digital Object Identifier 10.1109/81.269047
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3. **Overshoot control for two coupled RLC interconnections**
   Yang, Y.; Brews, J.R.;
   Components, Packaging, and Manufacturing Technology, Part B: Advanced Pa
   Transactions on [see also Components, Hybrids, and Manufacturing Technolo
   Transactions on]
   Volume 17, Issue 3, Aug. 1994 Page(s):418 - 425
   Digital Object Identifier 10.1109/96.311792
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4. **A coupled efficient and systematic full-wave time-domain macromodelin Simulation method for signal integrity analysis of high-speed interconne**
   Er-Ping Li; En-Xiao Liu; Le-Wei Li; Mook-Seng Leong;
   Advanced Packaging, IEEE Transactions on [see also Components, Packagin
   Manufacturing Technology, Part B: Advanced Packaging, IEEE Transactions o
   Volume 27, Issue 1, Feb. 2004 Page(s):213 - 223
   Digital Object Identifier 10.1109/TADVP.2004.825448
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1 Simulating and controlling the effects of transmission line impedance mismatches

Robert E. Canright

July 1986 **Proceedings of the 23rd ACM/IEEE conference on Design automation**

Publisher: IEEE Press

Full text available: pdf(623.55 KB) Additional Information: full citation, abstract, references, citings, index terms

Convenient equations for simulating the effects of transmission line impedance mismatches are derived from basic theory. These equations prove useful in controlling the transient response of a transmission line. The simulation equation for the load voltage is used to create design equations. Acceptable impedance mismatches are demonstrated in the context of criteria for acceptable transient voltages on a high-speed interconnection. The process of dividing the noise margin between ...

2 High-Frequency Nonlinear Amplifier Model for the Efficient Evaluation of Inband Distortion Under Nonlinear Load-Pull Conditions

G. Vandersteen, P. Wambacq, S. Donnay, F. Verbeyst

March 2002 **Proceedings of the conference on Design, automation and test in Europe**

Publisher: IEEE Computer Society

Full text available: pdf(236.15 KB) Publisher Site Additional Information: full citation, abstract

Designing complex analog systems needs differentabstraction levels to reduce the overall complexity. Therequired level of abstraction depends on the accuracy andthe purpose of the model. High-frequency amplifier modelscan vary from simple transfer functions for efficient bit-error-rate analysis up to detailed transistor leveldescriptions for accurate load-pull prediction. This paperintroduces a nonlinear black-box model for high-frequencyamplifiers. It extends the linear S-parameter representati ...

3 Reflections of high speed signals analyzed as a delay in timing for clocked logic

R. E. Canright, A. R. Helland

October 1987 **Proceedings of the 24th ACM/IEEE conference on Design automation**

Publisher: ACM Press

Full text available: pdf(573.82 KB) Additional Information: full citation, abstract, references, index terms

This paper develops equations that can extend the performance of high speed digital systems. The equations allow the application of timing analysis to the selection of the minimum series terminating resistor. Use of the minimum terminating resistor minimizes